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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10066603	FILING DATE 02/06/2002	CLASS 257	SUBCLASS	GAU 2844	EXAMINER
**APPLICANTS: Mori Kaoru; Matsumiya Masato;					
**CONTINUING DATA VERIFIED: <i>None</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-102175 03/30/2001 <i>Yes</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>[Signature]</i>		100353-00099			
TITLE : Semiconductor memory device with efficient and reliable redundancy processing <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-438L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Applicati n Examiner	
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